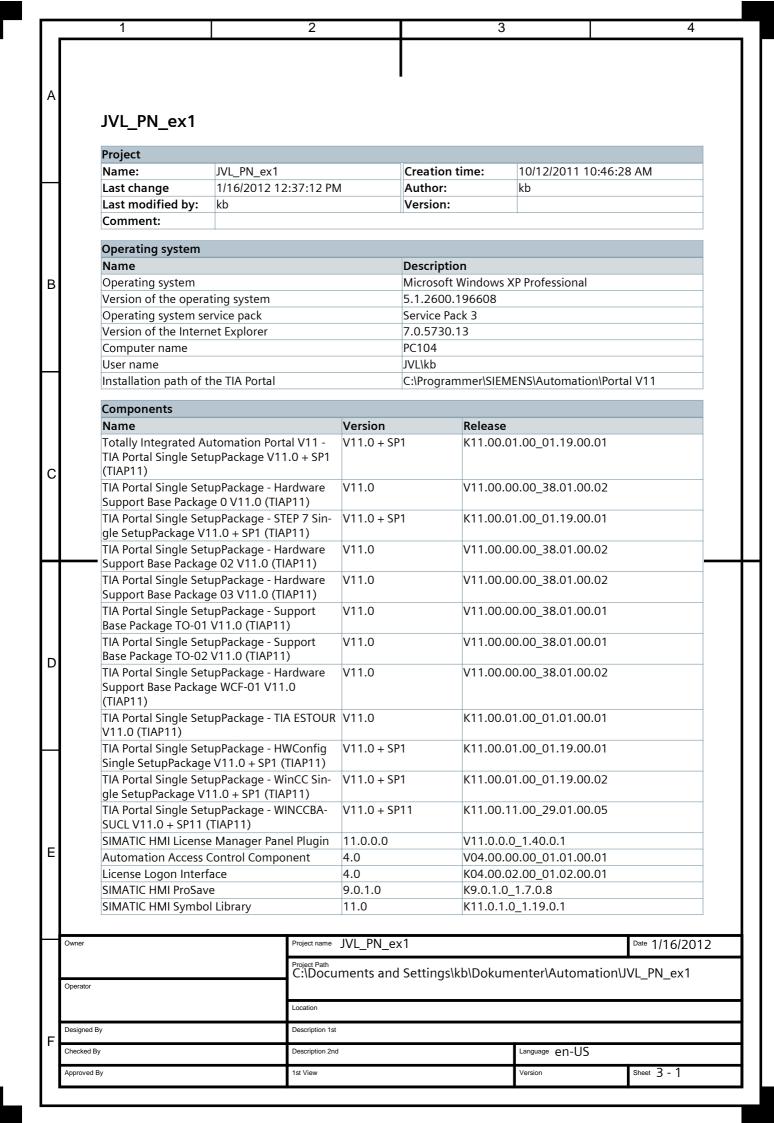
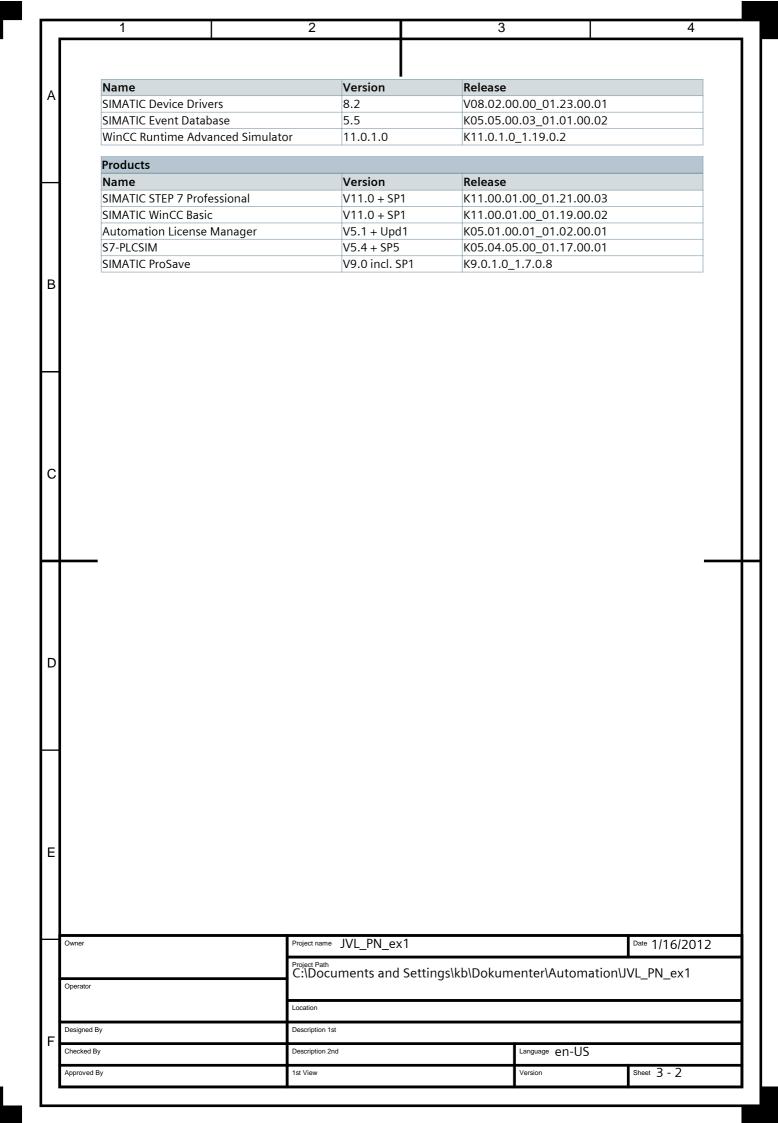


Table of contents JVL_PN_ex1 4 - 1 PLC_1 [IM151-8 PN/DP CPU] **Program blocks** Main [OB1] 5 - 1 Req_Read_DB [DB6] 6 - 1 Req_Write_DB [DB4] 7 - 1 8 - 1 Resp_Read_DB [DB5] System blocks В 9 - 1 **Program resources Technology objects** 10 - 1 **External source files** 11 - 1 **PLC tags** Default tag table [40] PLC tags 12 - 1 User constants 13 - 1 **PLC data types** 14 - 1 Watch and force tables С Force table 15 - 1 PLC alarms 16 - 1 PLC alarms 17 - 1 User diagnostic alarms 18 - 1 System alarms 19 - 1 **Text lists** 20 - 1 Common data Alarm classes 21 - 1 Text lists 22 - 1 D Languages & resources **Project languages** 23 - 1 Ε Project name JVL_PN_ex1 Date 1/16/2012 $\stackrel{\text{Project Path}}{\text{C:\Documents}}$ and Settings\kb\Dokumenter\Automation\JVL_PN_ex1 Operator Location Designed By Description 1st Checked By Description 2nd Language en-US Sheet 2 - 1 1st View Version Approved By

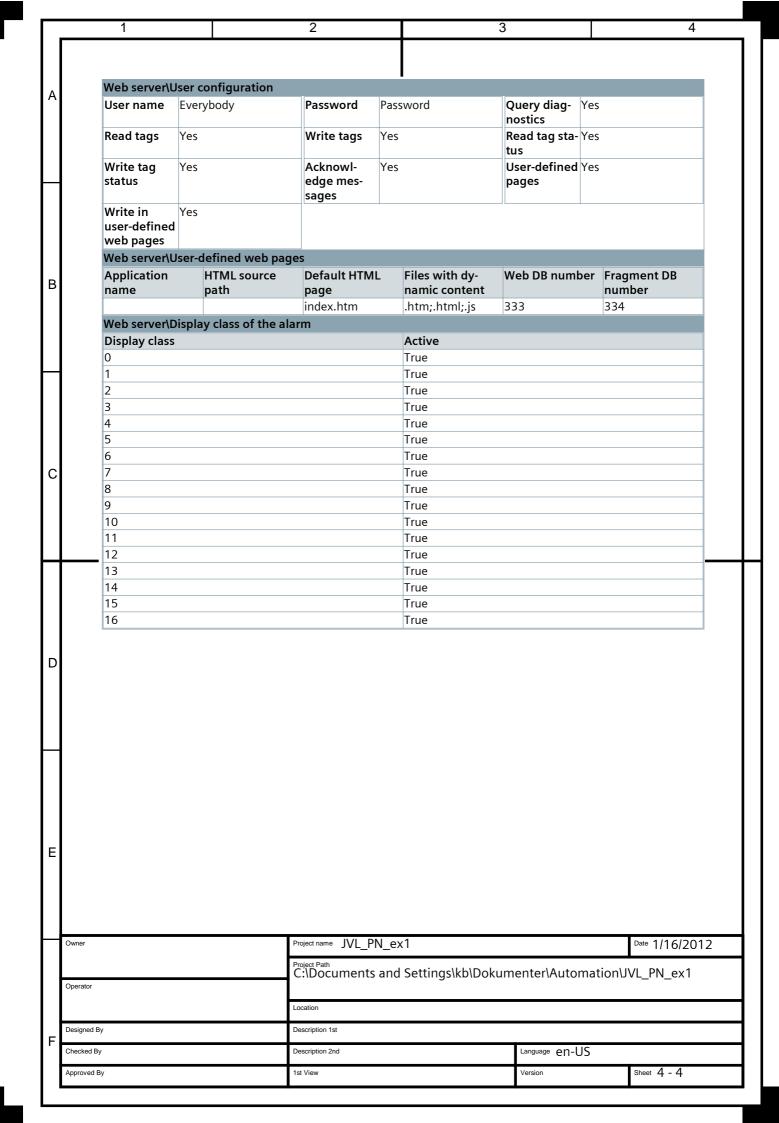


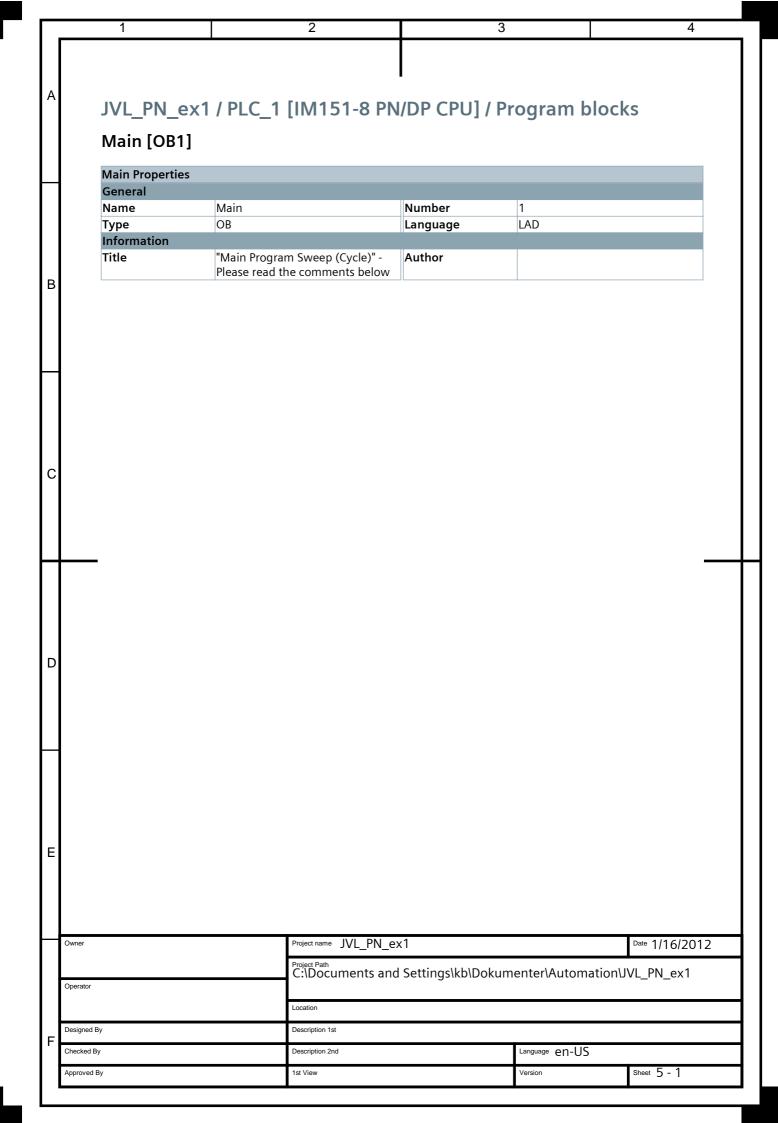


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В	Short designation		DP CPU		Description		J.	s; PROFINET inter- ication (loadable ET IO controller; B ports; MRP; ROFINET CBA isport protocol, CP; data record a V3.2; expansion
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	Order number	6ES7 151-8A	BO1-OARO		Firmware versi		ET200S spectrum V3.2	<u> </u>
	General\Identificat				versi			
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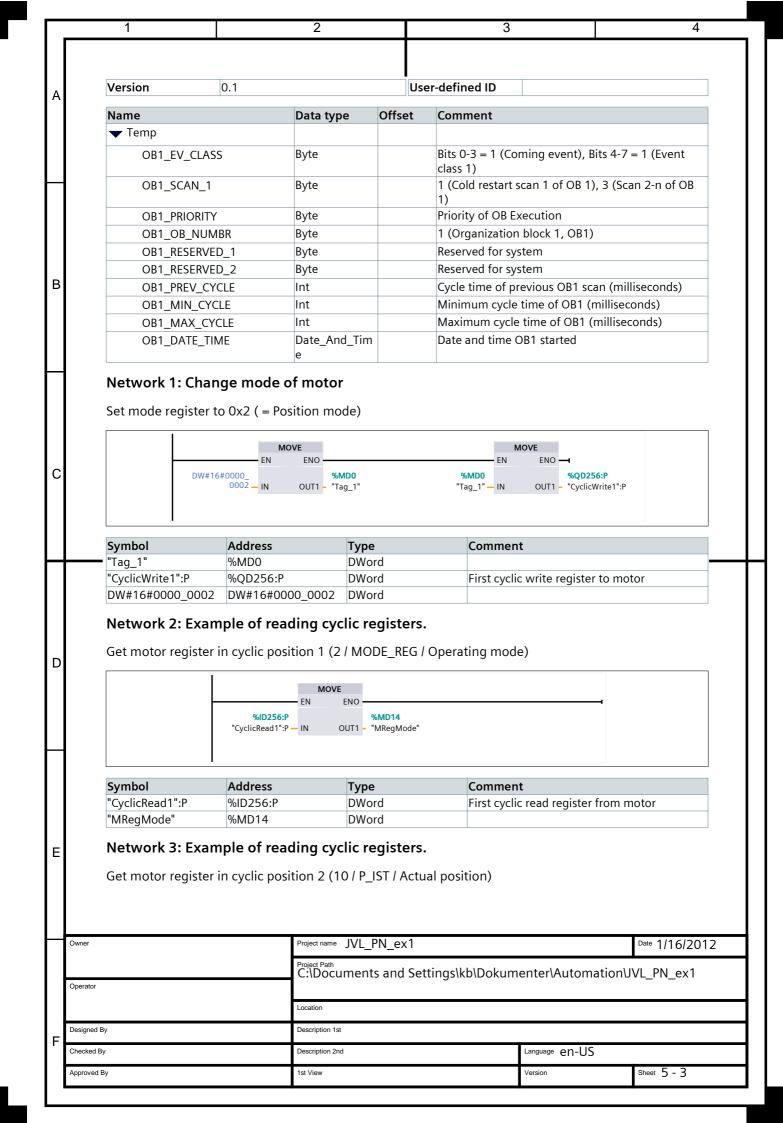
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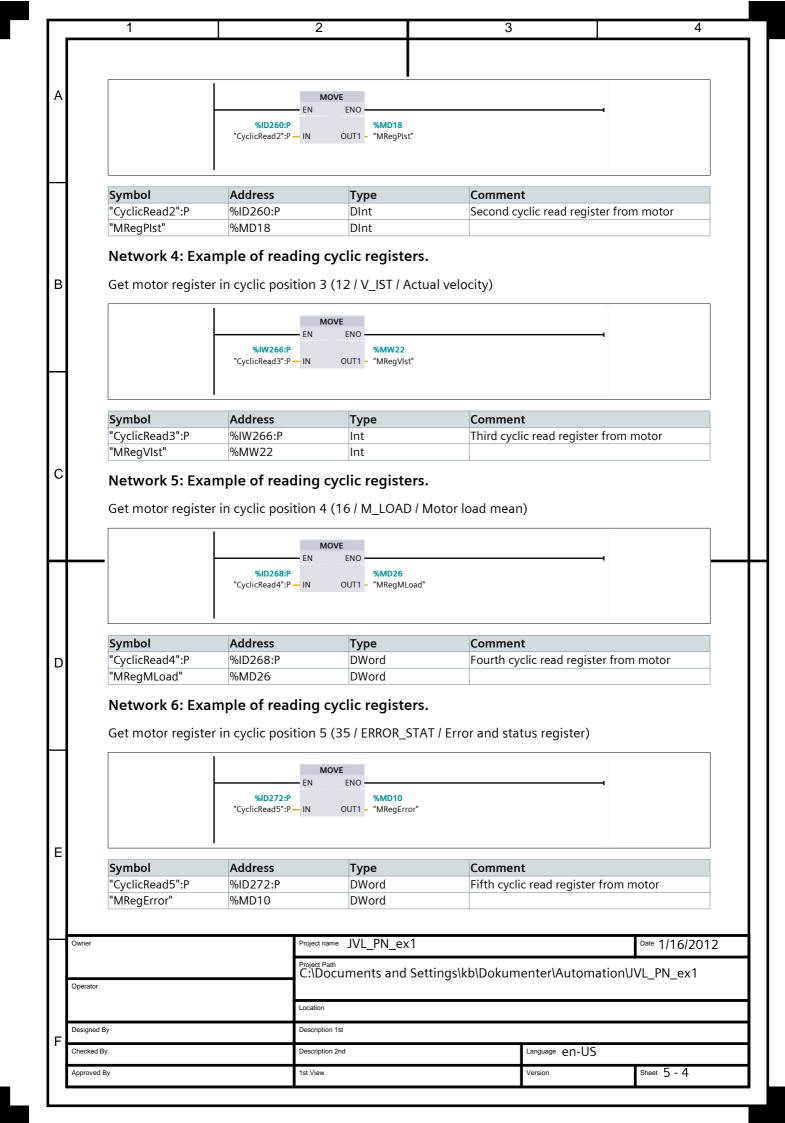
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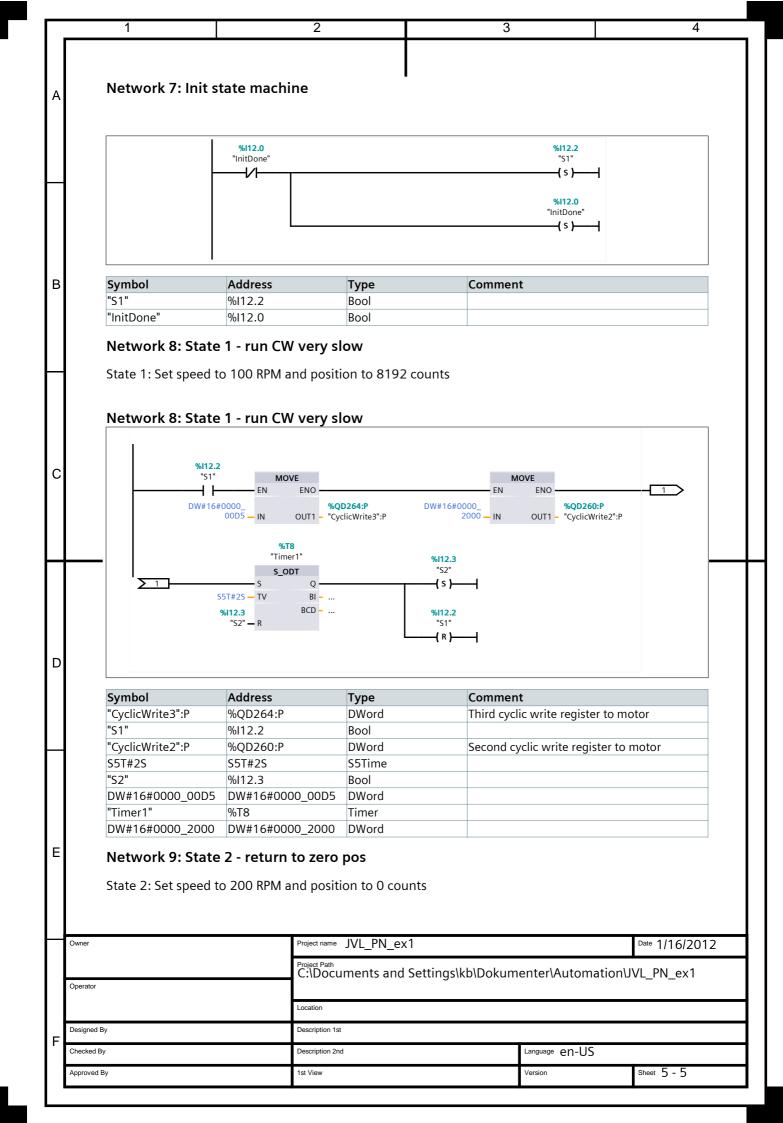


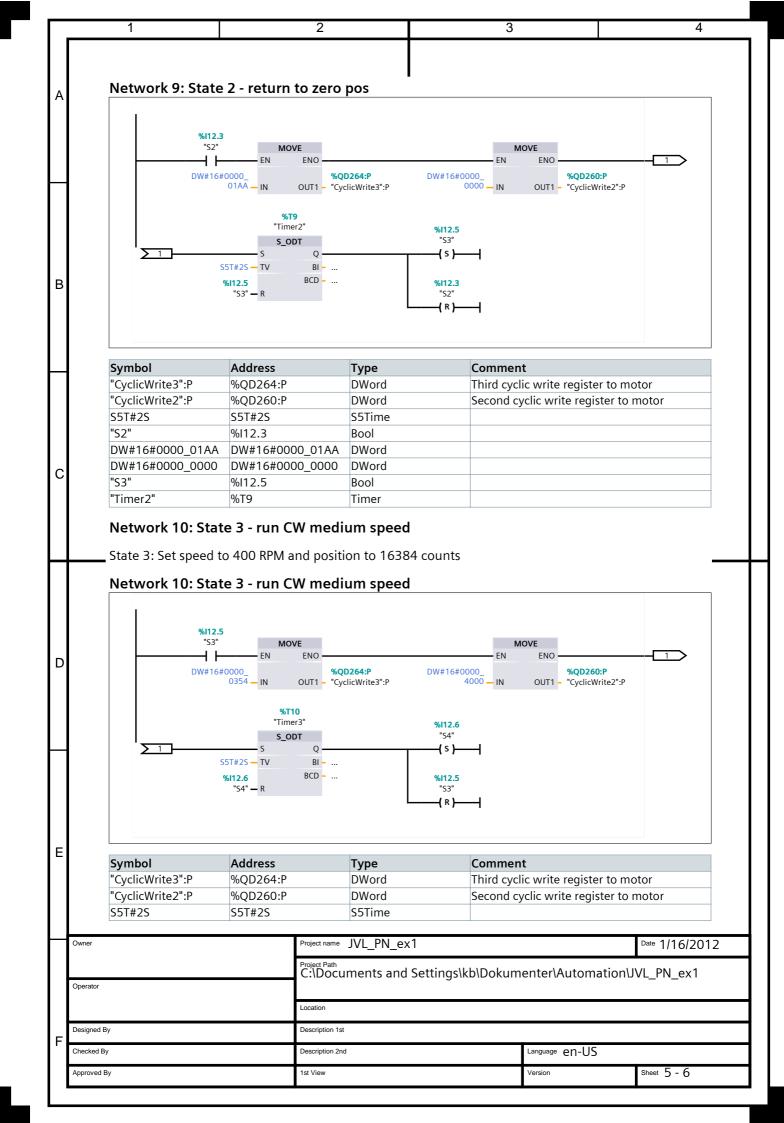


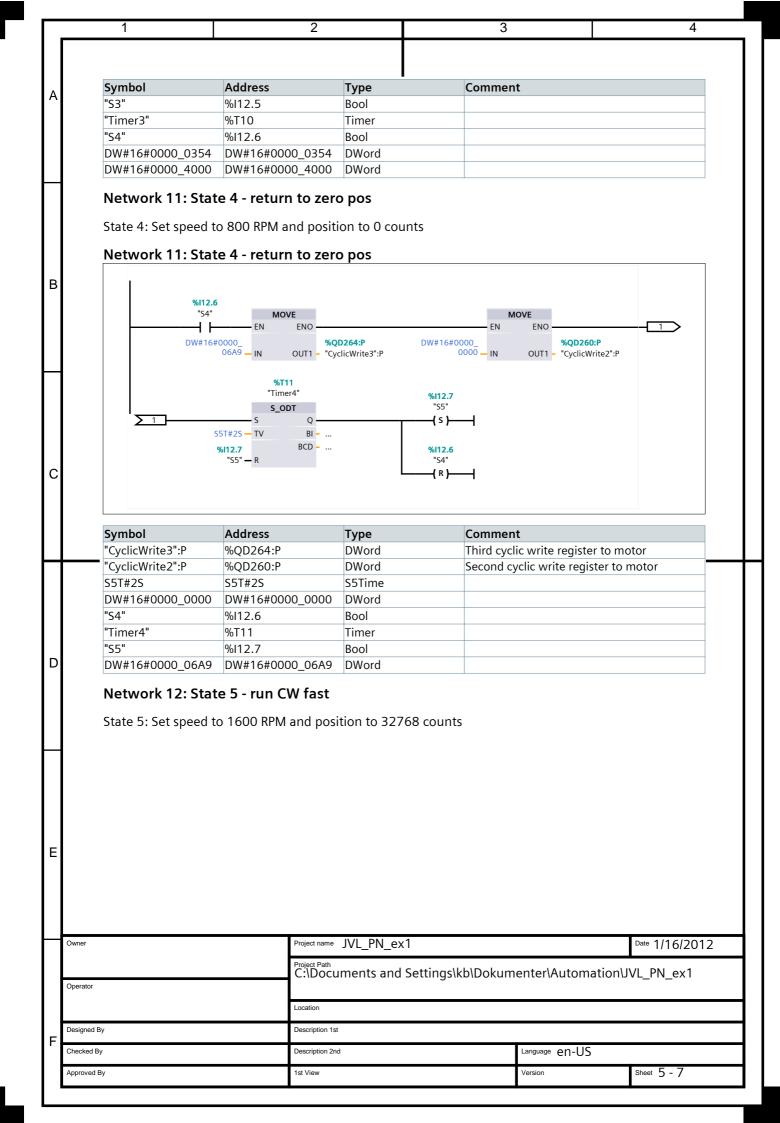
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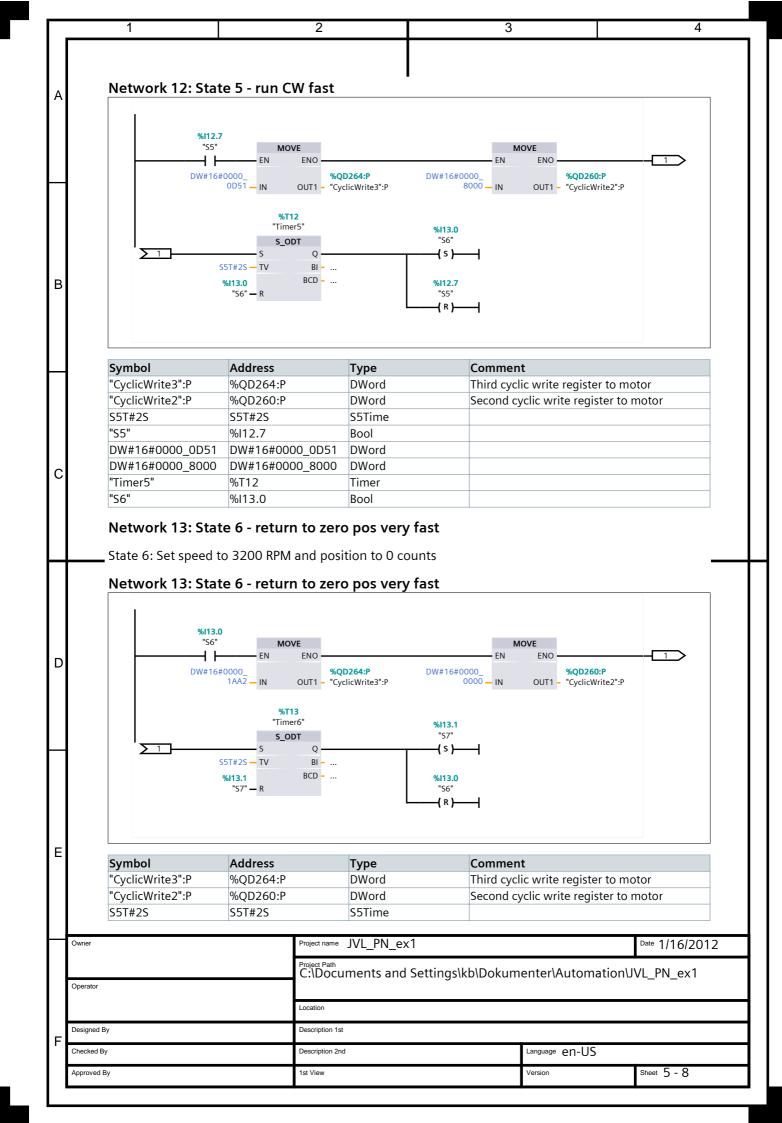


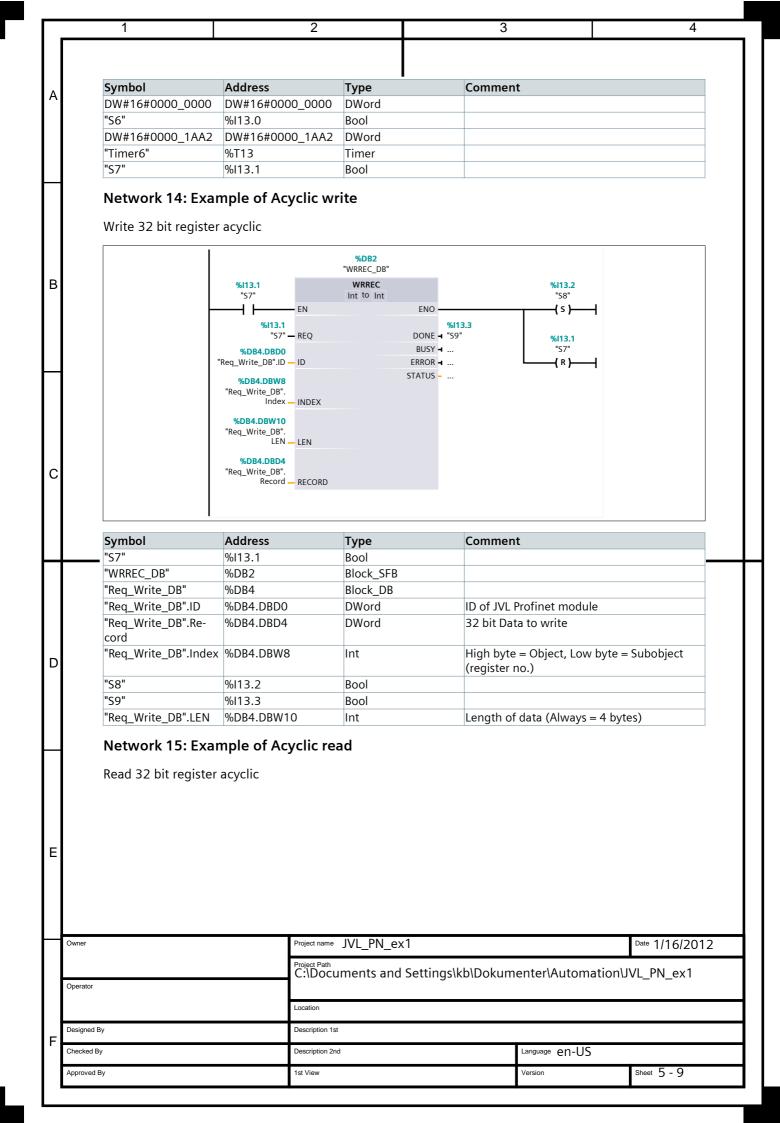


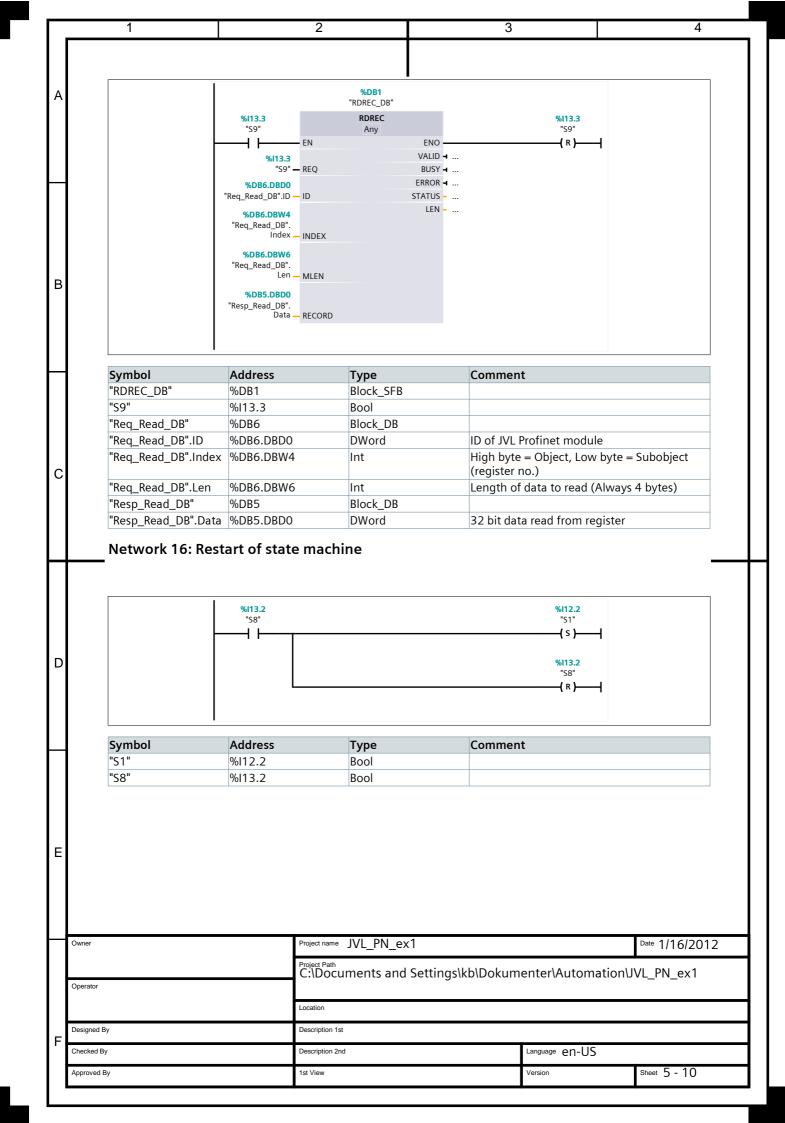










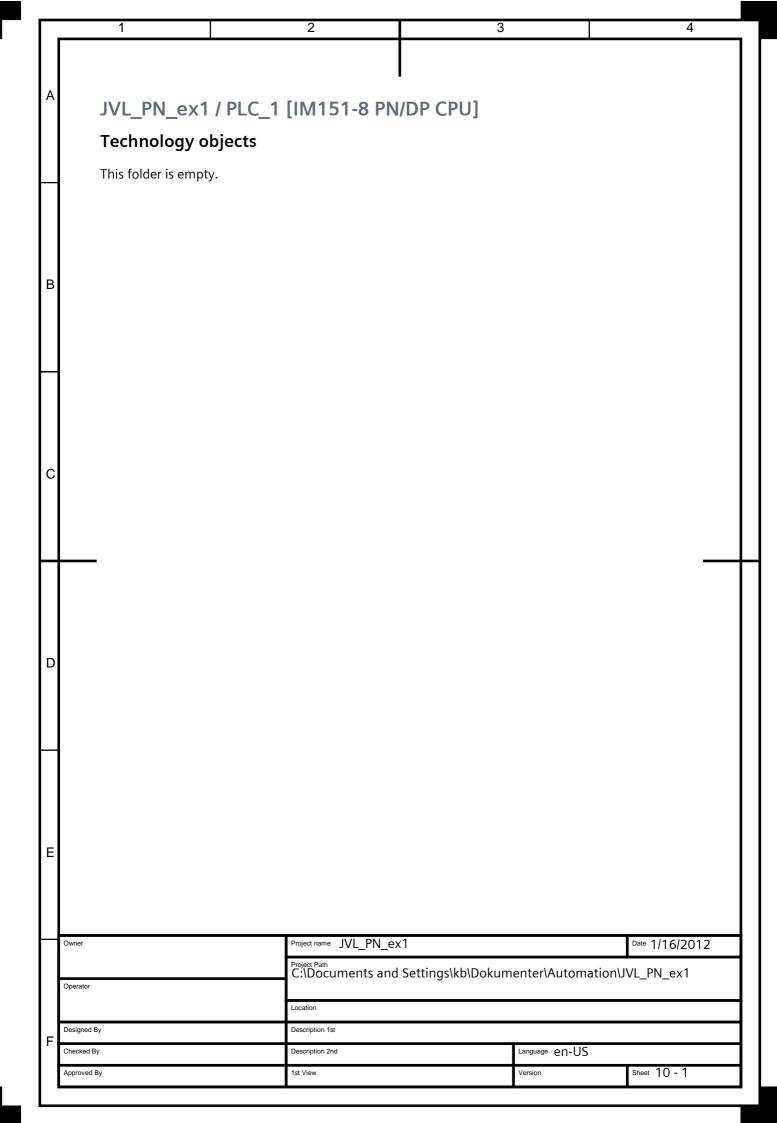


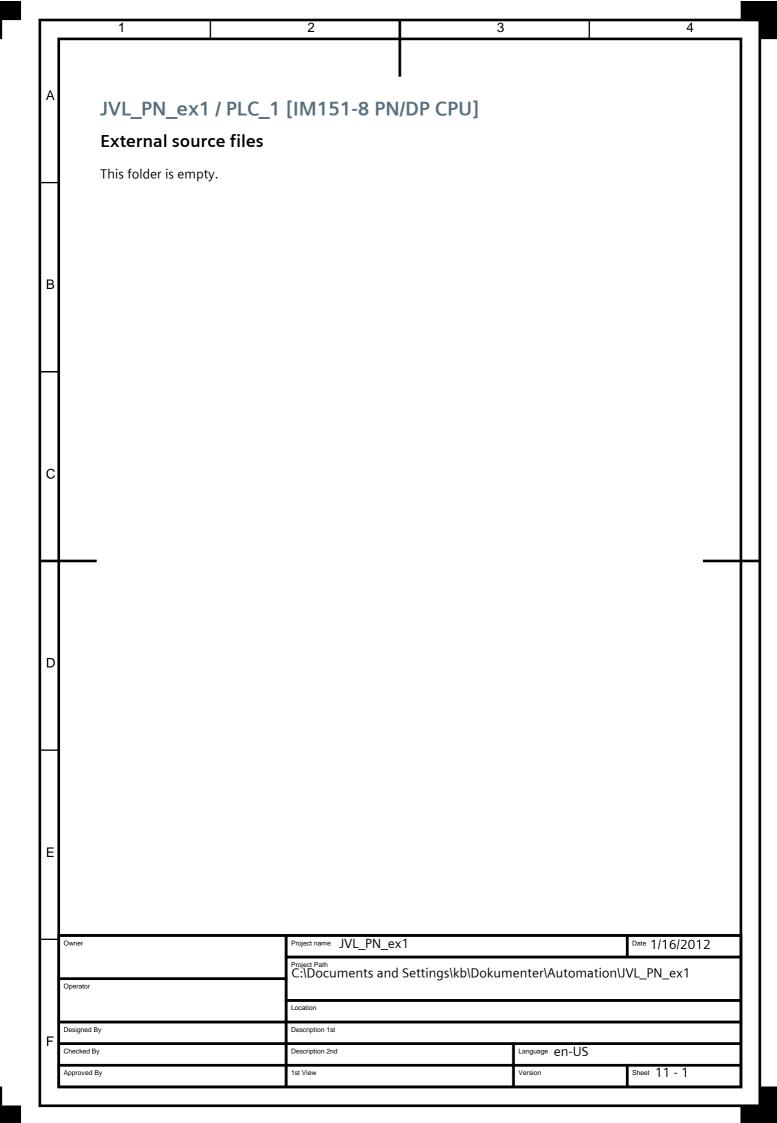
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ID	DWor		2042	True	True	True	ID of JVL Profinet module
Index	Int	4.0	16#1233	True	True	True	High byte = Object, Low byte = Subobject (regis- ter no.)
Len	Int	6.0	4	True	True	True	Length of data to read (Always 4 bytes)

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		ID	DWord	0.0	2042	True	True	True	ID of JVL Pr	ofinet module	
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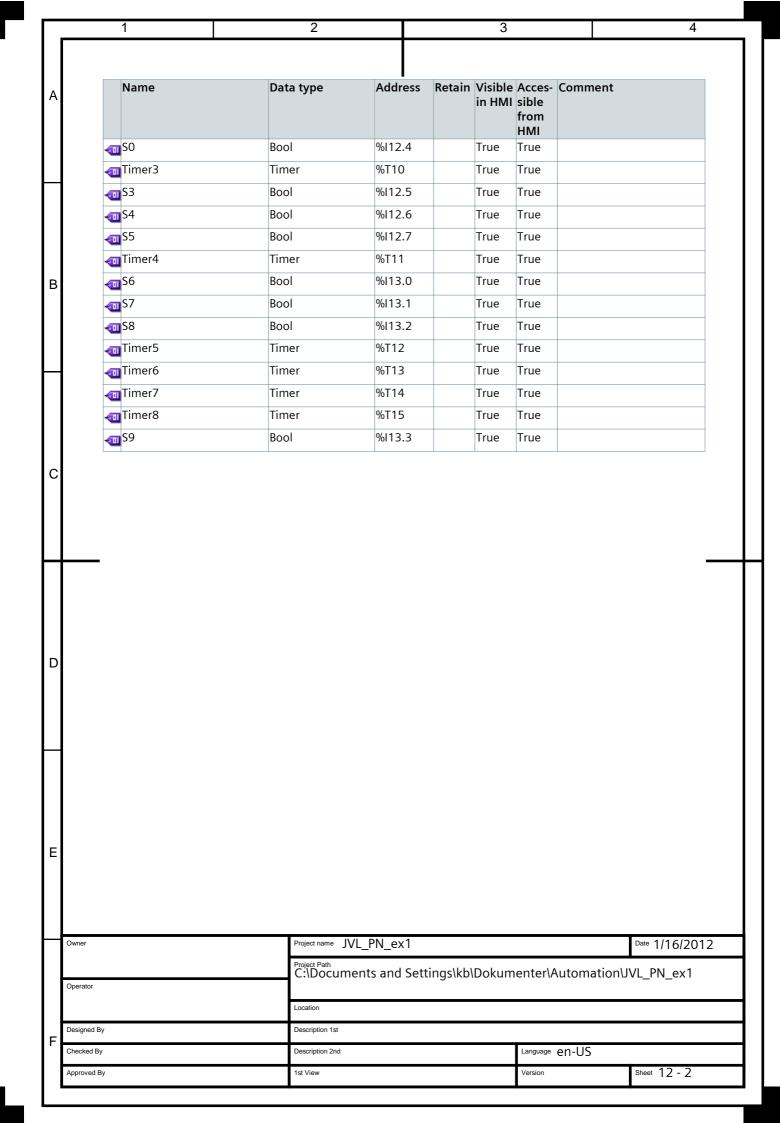
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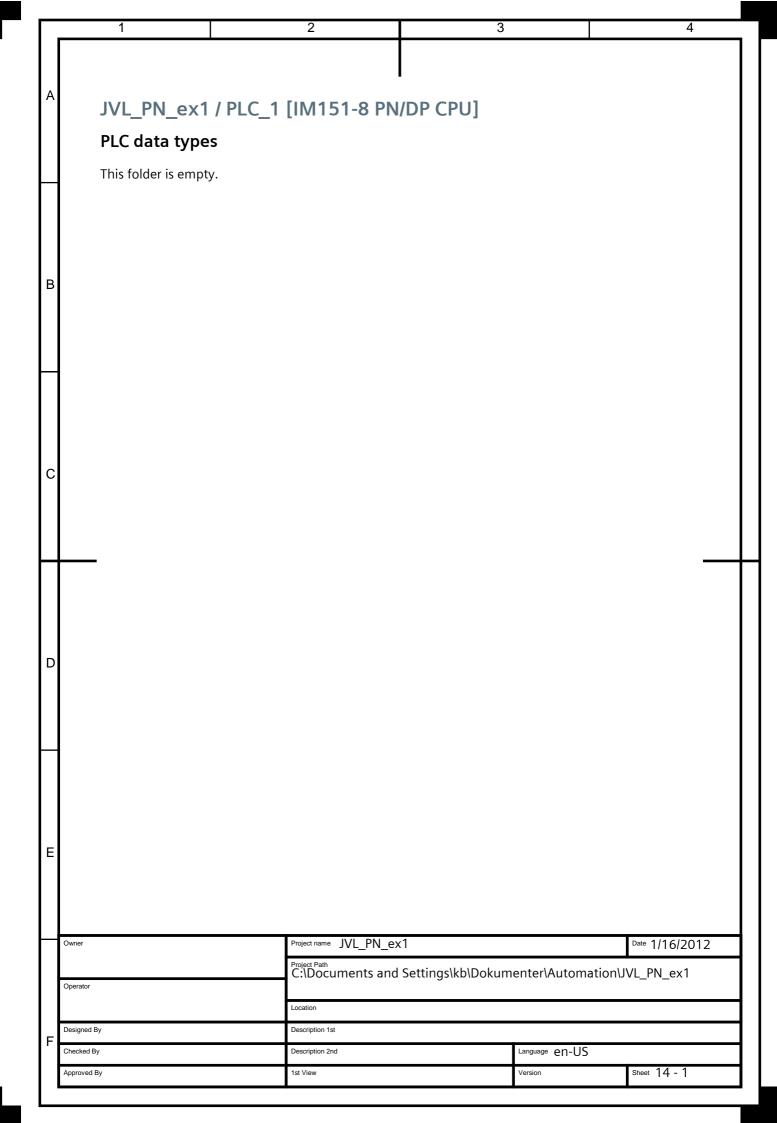




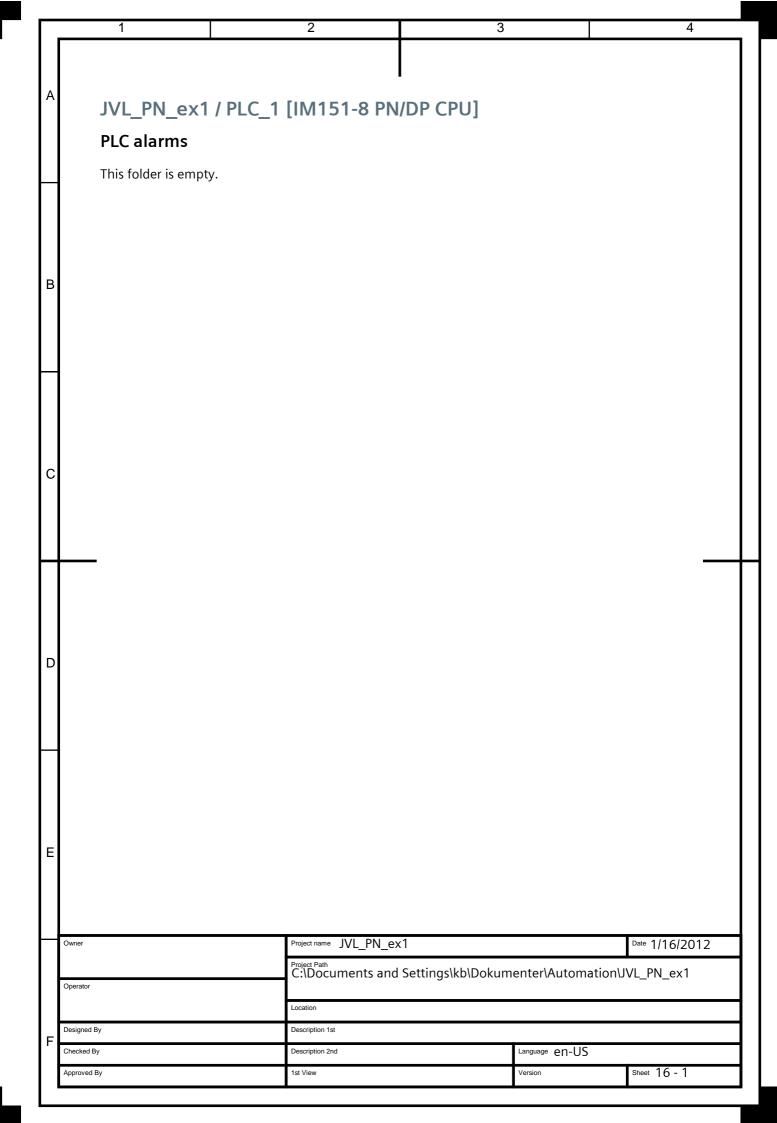
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	CyclicWrite2	DWord	%QD260		True	True	Second cyclic w to motor	rite register
	CyclicWrite3	DWord	%QD264		True	True	Third cyclic writ to motor	e register
	CyclicWrite4	DWord	%QD268		True	True	Fourth cyclic wr to motor	_
	CyclicWrite5	DWord	%QD272		True	True	Fifth cyclic write motor	e register to
	← CyclicRead1	DWord	%ID256		True	True	First cyclic read from motor	
	← CyclicRead2	DInt	%ID260		True	True	Second cyclic re from motor	ead register
	← CyclicRead3	Int	%IW266		True	True	Third cyclic read from motor	d register
	CyclicRead4	DWord	%ID268		True	True	Fourth cyclic rea	ad register
	← CyclicRead5	DWord	%ID272		True	True	Fifth cyclic read from motor	register
	₹ag_2	Int	%MW100		True	True		
	₹ag_3	DWord	%MD4		True	True		
		Bool	%M8.0		True	True		
	← m Timer1 Timer	Timer	%T8		True	True		
	₁ Timer2	Timer	%T9		True	True		
	■TempTime	S5Time	%IW10		True	True		
	an InitDone	Bool	%I12.0		True	True		
	DirectionCW	Bool	%I12.1		True	True		
	■MRegError	DWord	%MD10		True	True		
	■ MRegMode	DWord	%MD14		True	True		
	₹ MRegPlst	DInt	%MD18		True	True		
	₹ MRegVIst	Int	%MW22		True	True		
	■ MRegMLoad	DWord	%MD26		True	True		
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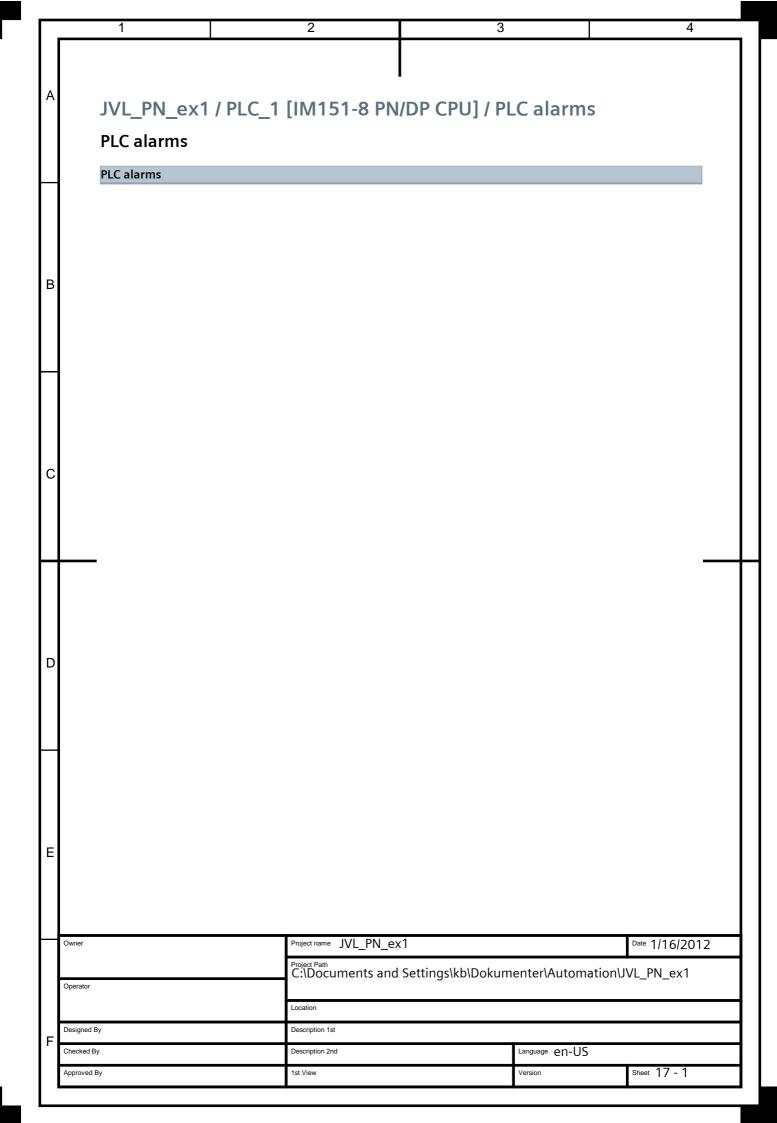


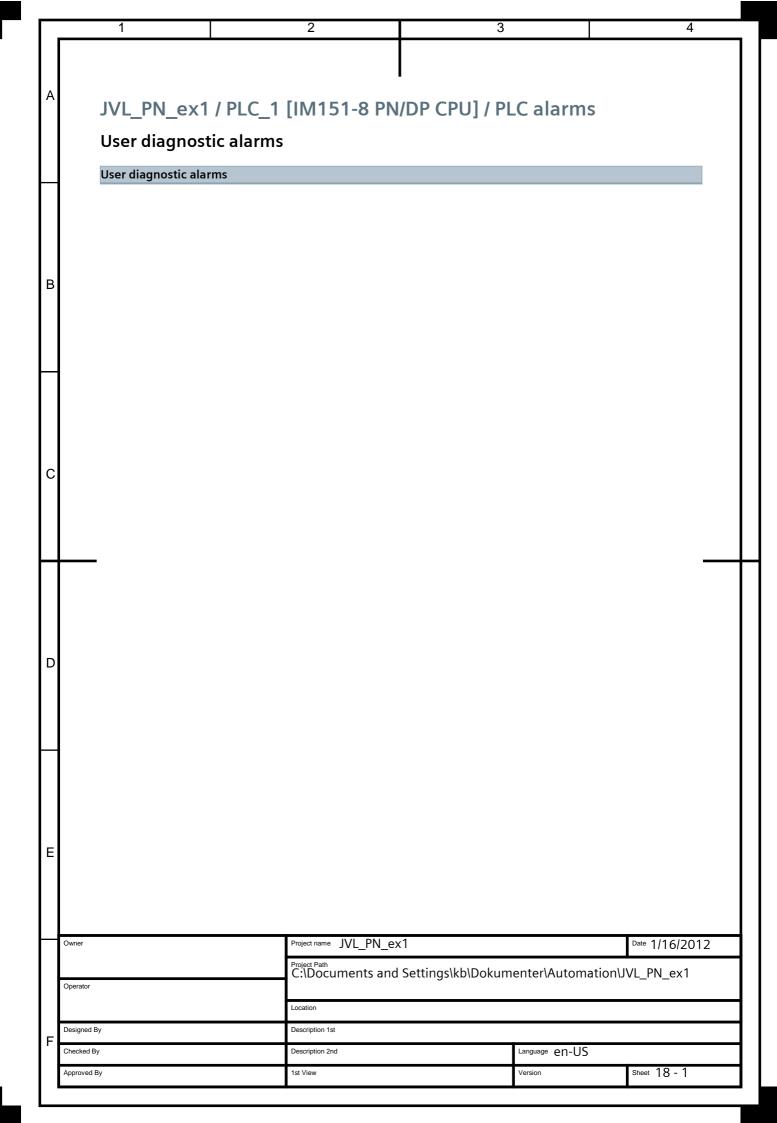
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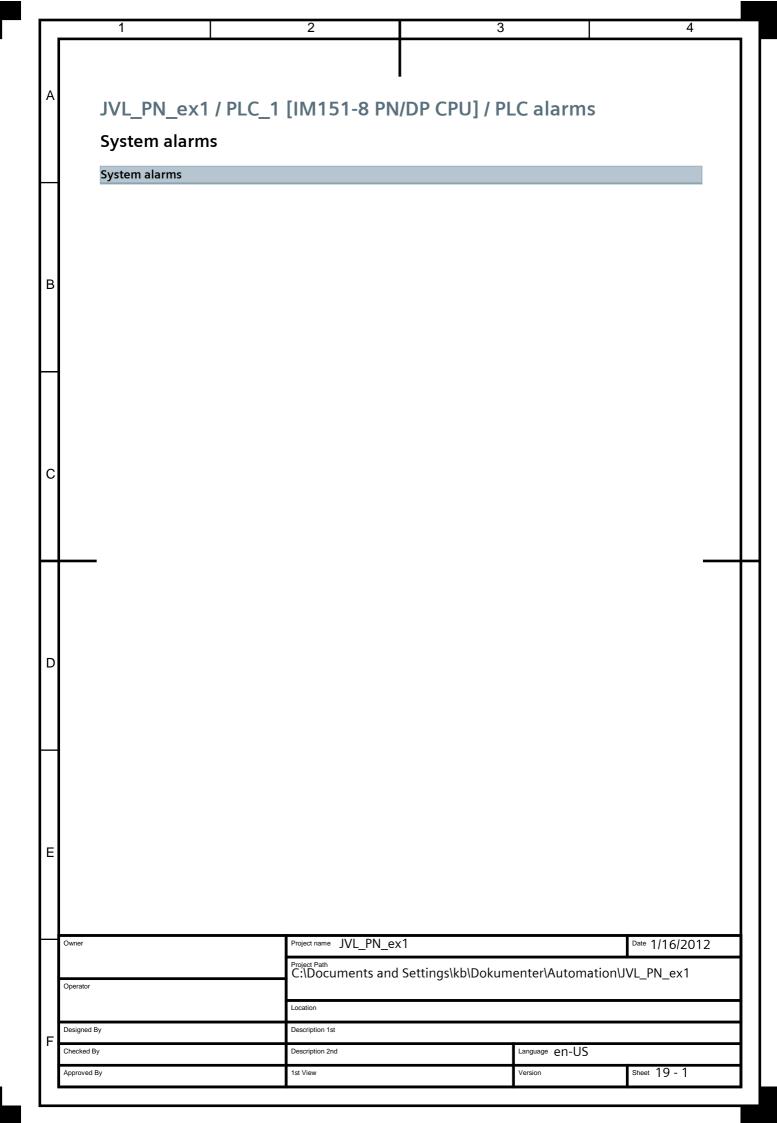


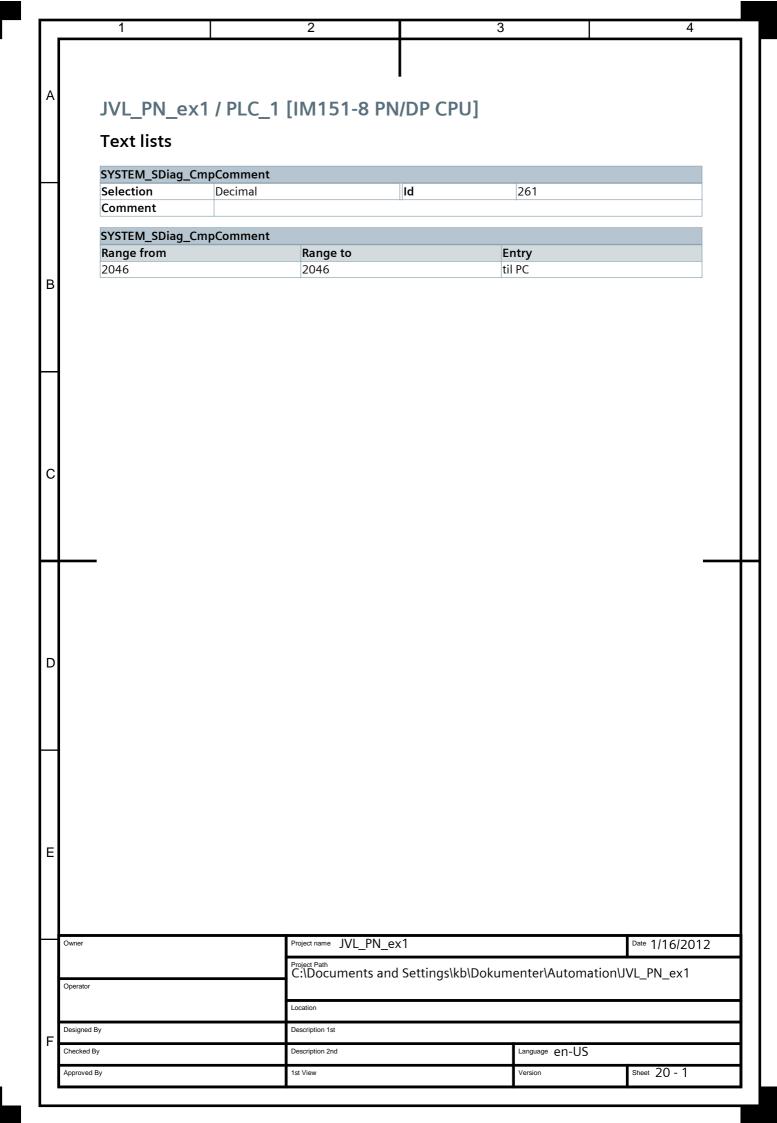
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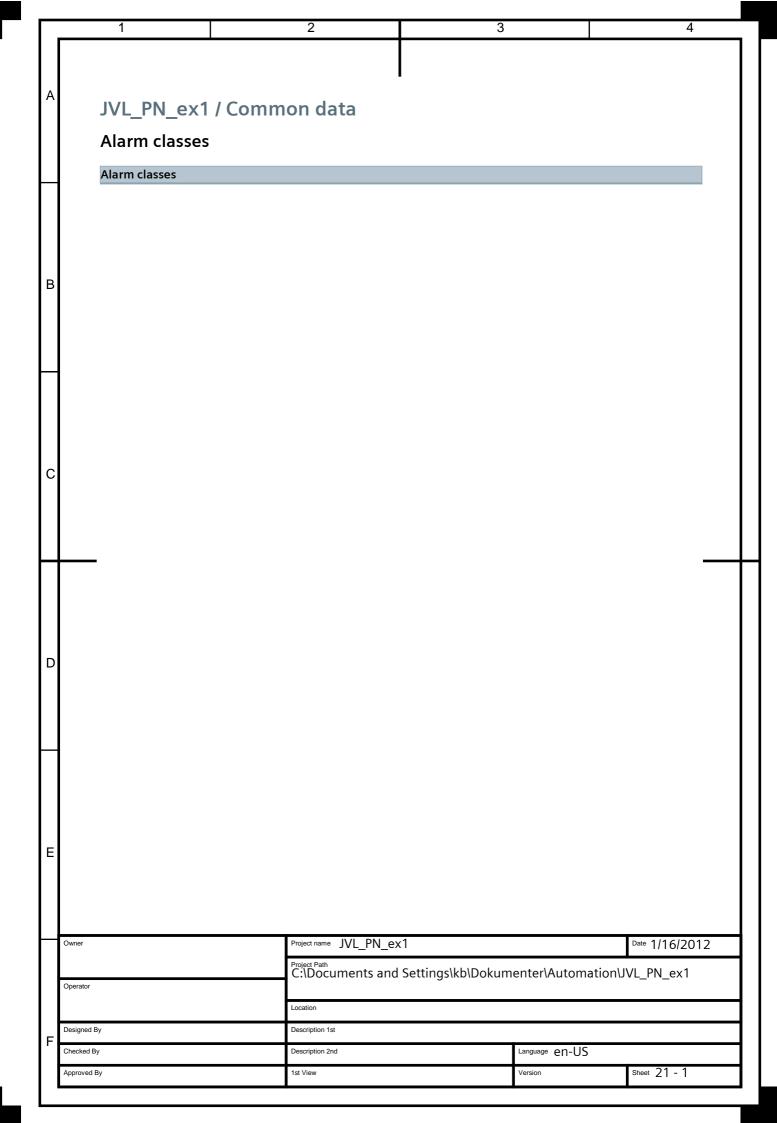


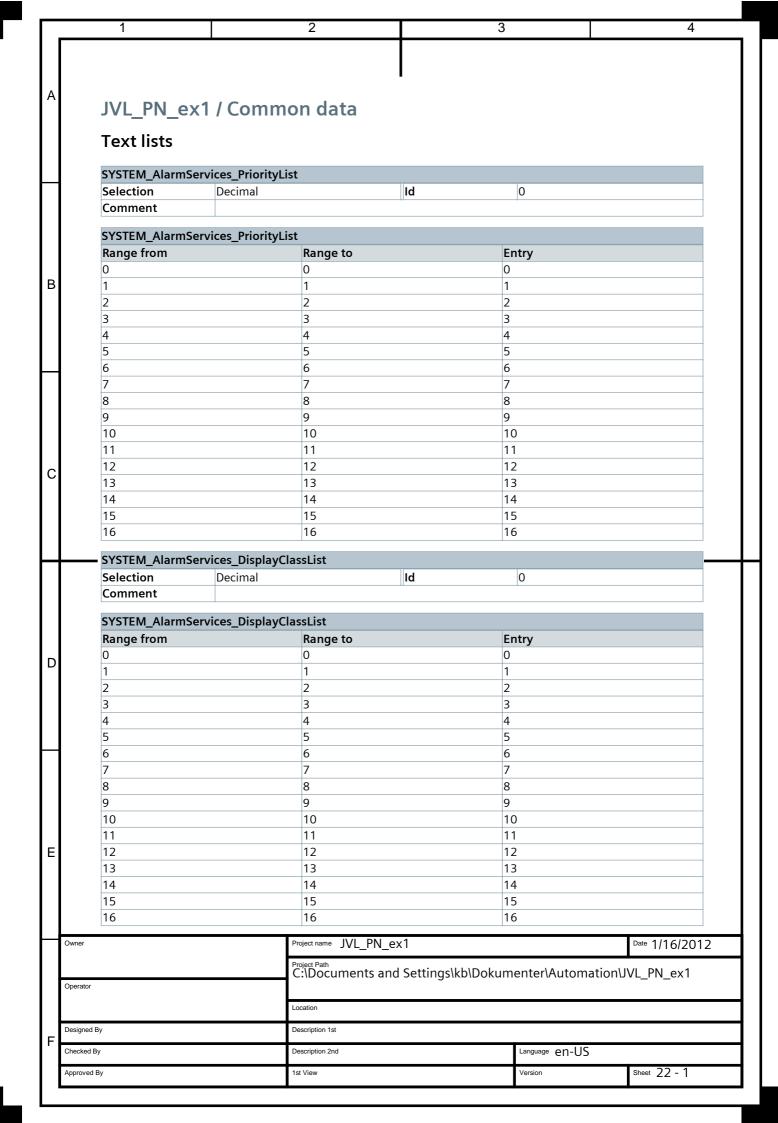




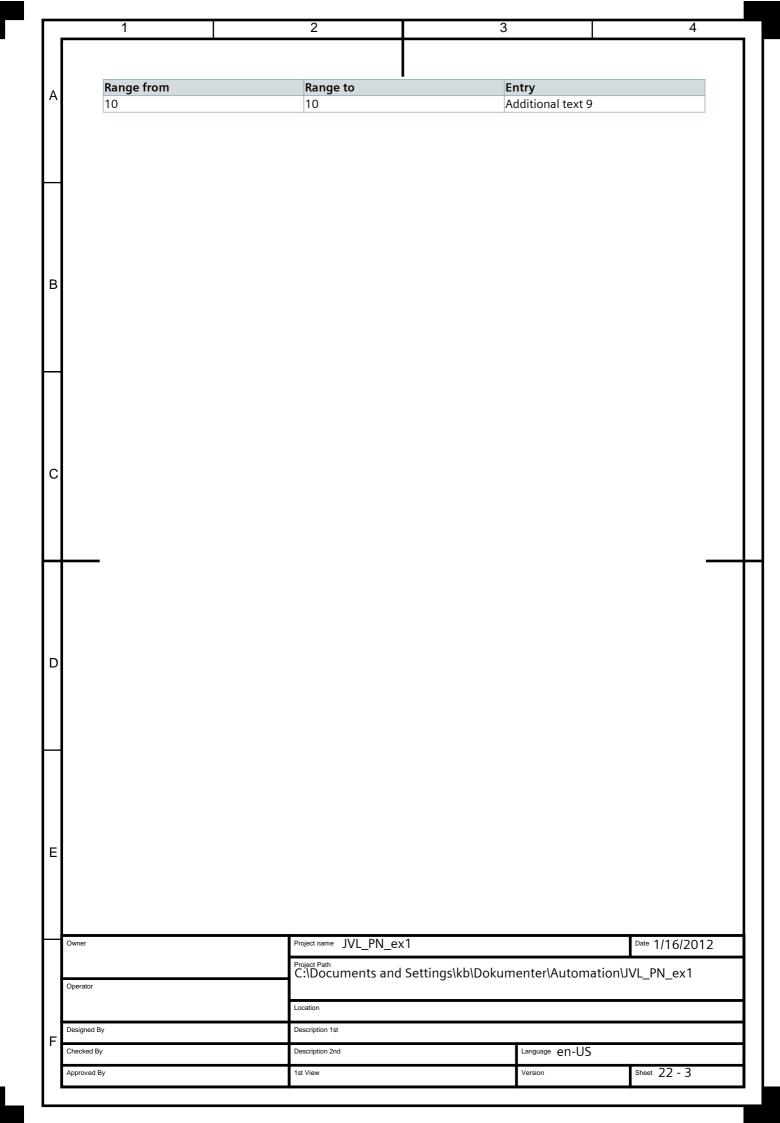








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		Services_Producerl				
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	Project languages					
Н	Languages Reference language					
	English (United States)					
	Editing language					
	English (United States)					
	Other project languages					
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		Location				
Design	signed By Description 1st					
F	Description 2nd Language en-US					
Approv		1st View		Version	Sheet 23 - 1	
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